

AVRADCOM

Technical Report - 79-0270-5

ELECTRONIC MASTER MONITOR AND ADVISORY DISPLAY SYSTEM TEST AND DEMONSTRATION REPORT

GENERAL ELECTRIC COMPANY AIRCRAFT EQUIPMENT DIVISION BINGHAMTON, NY 13902

June 1981

FIFTH INTERIM REPORT FOR PERIOD COVERING JAN 81 - Jun 81

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This report documents the testing the feasibility Electronic Master (EMMADS)	of the hardware	and software provided for isory Display System

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PREFACE

This document establishes the requirements for contractual acceptance of the EMMADS Hardware supplied by General Electric under contract #DAAK 80-79-C-0270. The acceptance test proceedings will be held at the General Electric facility in Binghamton, New York. The individual tests are to be witnessed by a designated representative of the US Army Aviation Research and Development Command, Communications/Sensor Division, Instrumentation Branch, Fort Monmouth, New Jersey.

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(TermiNet $^{\rm R}$ is a registered trademark of the General Electric Company) ii

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1.0 INTRODUCTION

This Test and Demonstration Report is submitted in compliance with the EMMADS statement of work and contract data requirements. The purpose of the contract is to demonstrate the feasibility of an Electronic Master Monitor and Advisory Display System (EMMADS). The purpose of this report is therefore to document the testing of the hardware and software provided by the contractors when used in a system environment to demonstrate the feasibility of an EMMADS.

2.0 TYPE OF TESTING PERFORMED

By the statement of work and directive of Army personnel, the EMMADS program was to be a conceptual study of the EMMADS concept, and thus to design and fabricate a programmable feasibility model. However, the emphasis was to be placed on use of available hardware designs wherever possible to construct a system capable of demonstrating the concept. Testing was performed to an "Acceptance Test Procedure" developed with those objectives in mind.

Test 1.0 <u>Physical Characteristics</u> was intended simply to document the size and weight of all components.

Test 2.0 Workmanship was included to indicate Army acceptance of the general construction and workmanship of the various components. The equipment design and construction techniques are consistent with flight quality hardware. No environmental tests were required or conducted on any equipment under this contract.

The remainder of the tests were all related to System Verification. The first group of seven (7) tests are grouped under section 3.1 Component/Package Functional Verification and verify the functions of the various system modules. The last group of twelve (12) tests are grouped under section 3.2 Integrated Operational Verification and verify the various system operational modes.

3.0 TEST RESULTS

The complete Acceptance Test was performed at Ft. Monmouth on March 20, 1981. Tests were conducted by General Electric personnel and witnessed by the contract technical representative, who initialed each test procedure. No exceptions were taken on any test. Attached is a copy of the completed Acceptance Test Procedure with completion date and initials.

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(NOTE: Revised material is indicated by a vertical hash mark on the right page margin. The absence of a hash mark indicates a new page or a complete page change.)

TITLE: Physical Characteristics

PURPOSE: Document the size and weight of all components.

INSTRUMENTATION:

- 1. Surface plate
- 2. Height gage
- 3. Scale

TEST PROCEDURE:

- Place each item on surface plate and measure maximum dimensions with height gage.
- 2. Weigh each item on scale.

ACCEPTANCE CRITERIA: Dimensions and weight shall be recorded below.

ITEM	WEIGHT (#)	DIMENSIONS (HWD)
Test Panel	22.1	7,2x19.2 x 12.2 + Mondio
MCP-701A (Emulator)	38:	7.6×10.3×19.6 +Hadle
MCP-701A (EMMADS)	39.	7.6 x 10.3 x 19.6 + Handle
Display Panel	11.4	10.6x 11.3 × 6-3
Keypad (TERMIFLEX TM Control/Display Unit)	1.3	7.0 × 4.2 × 2.1

TEST HISTORY:

DATE 3-20-81 STATUS OF WITNESS PAR

(TERMIFLEX TM is a trademark of the Termiflex Corporation)

TEST NUMBER: 2.0

TITLE: Workmanship

PURPOSE: Verify acceptable workmanship of all applicable EMMADS

components.

TEST PROCEDURE: R&QA shall inspect all assemblies and subassemblies for proper workmanship and shall affix a stamp to each unit inspected which verifies the unit has met the appropriate inspection criteria.

ACCEPTANCE PROCEDURE: All assemblies and subassemblies shall be visually checked for the certification stamp affixed by R&QA.

TEST HISTORY:

DATE 3 228 STATUS OF WITNESS OF

TITLE: MCP-701A Computer Diagnostic Functional Verification

<u>PURPOSE</u>: Demonstrate that the MCP-701A computers do the basic computation and decisions required to operate the EMMADS system

in a software programmable manner.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: RS-232/C and symbol generator interrupts.

INSTRUMENTATION: None.

TEST PROCEDURE: No specific procedure required.

ACCEPTANCE CRITERIA: Observe that the computer responds as necessary to perform the tests of the remainder of Section 3.

TEST HISTORY:

DATE 3-20'-5' STATUS OF WITNESS FUL

TITLE: TermiNet 200 MSR Printer Functional Verification

PURPOSE: Verify the operating capability of the TermiNet 200

MSR (Magnetic Send Receive) Printer.

<u>HARDWARE CONFIGURATION</u>: TermiNet 200 MSR Printer and EMMADS

MCP-701A with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS.

TEST PROCEDURE:

- 1. Power up system in normal configuration.
- 2. Use the TermiNet 200 MSR Printer to execute the operating system functions per the MCP-701A Operating System Manual (ACS 11,357) and cassette tape procedures.

ACCEPTANCE CRITERIA: The TermiNet 200 MSR Printer and EMMADS system shall respond per the documents referenced in the Test Procedure.

TEST HISTORY:

DATE 3-20-8' STATUS CHE WITNESSPUL

TITLE: Test Panel/Emulator Functional Verification

PURPOSE: Demonstrate the capability of the Emulator to cause analog and discrete inputs from the test panel to be transmitted on the 1553B data bus.

HARDWARE CONFIGURATION:

1. Data Bus Monitor/Controller

(DBMC)

2. Remote Transmission Unit Emulator

(RTU-EM)

3. System Test Panel

SOFTWARE REQUIRED:

- 1. Software to cause DBMC to act as a bus controller
- 2. RTU-EM Operational program

STIMULI: Various test panel pots and switches. (See Fig. 3.1.3) TEST PROCEDURE:

- 1. Power up the RTU-EM, the DBMC and the system test set.
- 2. Observe the capability to vary the data displayed by the DBMC using test panel inputs and for each input change made, compare the location of the varying data on the display with that shown in Table 3.1.3(a)

ACCEPTANCE CRITERIA: Observe that data from the test panel can be placed on the bus and varied in accordance with locations defined in Table 3.1.3.

TEST HISTORY:

DATE 3-20-81 STATUS CIKE WITNESS RUF-

DBMC DISPLAY - MAP OF TEST PANEL INPUTS

= ANALOG/DIGITAL INPUT; DIN = DISCRETE INPUT)

(X - DON'T CARE)

(ADIN

					ا خ	(Y = DON I CHANE)	INE.)						
GI SUR	G. C.	DEST ADD ADIN02	ADI NO2	ADIN03	ADIN06	ADIN07	ADIN08	ADIN09	ADIN10	ADIN11	ADIN12	ADIN13	ADIN14
			ADIN16	ADIN17	ADIN18	ADIN19	ADIN20	ADIN21	ADIN22	ADIN26	ADIN27	ADIN29	ADIN28
		ADIN27	ADIN29	ADIN28	0000	ADIN01	ADIN04	ADIN05	0000	0000			
BUS ID	CMIND	DEST ADD	0000	0000	ADIN23	ADIN23	ADIN24	0000	0000	0000	0000	0000	ADIN30
		ADIN30	ADIN31	ADIN31	RWD1	RWD2	RWD3	RWD4					

DEFINITION OF DISCRETE WORDS SHOWN ABOVE

BIT VALUE

				NIQ)	# INDI	CATES T	EST PAN	EL DISC	RETE IN	our con	FROLLING	(DIN # INDICATES TEST PANEL DISCRETE INPUT CONTROLLING THAT BIT VALUE)	T VALUE			
	80	B1	B2	B 3	B4	B5	B6	B7	B8	B9	B1 0	B11	B12	B13	B14	_
RWD1	DINZO		DIN21 PIN22	DIN23 DIN24	DIN24	DIN25	DIN25 DIN26 DIN27 DIN28	DIN27	DIN28	DIN29	DIN30	DIN31	0	0	0	
RWD2	0		IN14	DIN15 0	0	0	0	0	0	0	0	0	0	0	0	
RATD3	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	
RWD4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	DIN32	

B15

TABLE 3.1.3(a)

WORD

(See Table 3.1.3(b) For Test Panel Parameter locations)

INPUTS OB OB OB OB OB OB OB OB OB O		S	2 33 34 146 51 C1 A11 A12 U1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	GEREBAI (B) EIECTBIC (C)
ANALOG ON	$\bigcirc \bigcirc $			@ ANALOG - DISCRETE I/O INTERFACE

FIGURE 3.1.3

TEST PANEL INPUT	CONTROLLED PARAMETER
ADINOO	Eng 1 N ₁
ADIN01	Eng 2 N ₁
ADINO2	Eng 1 TGT
ADIN03	Eng 2 TGT
ADINO4	Eng 1 Torque
ADINO5	Eng 2 Torque
ADINO6	Eng 1 Oil Pressure
ADIN07	Eng 2 Oil Pressure
ADIN08	Eng 1 Oil Temperature
ADIN09	Eng 2 Oil Temperature
ADIN10	Eng 1 XMSN Oil Pressure
ADIN11	Eng 2 XMSN Oil Pressure
ADIN12	Combining XMSN Oil Pressure
ADIN13	Forward XMSN Oil Pressure
ADIN14	Aft XMSN Oil Pressure
ADIN15	Eng 1 XMSN Oil Temperature
ADIN16	Eng 2 XMSN Oil Temperature
ADIN17	Combining XMSN Oil Temperature
ADIN18	Forward XMSN Oil Temperature
ADIN19	Aft XMSN Oil Temperature
ADIN20	System l Flight Control Hydraulic Pressure
ADIN21	System 2 Flight Control Hydraulic Pressure
ADIN22	Utility Hydraulic Pressure
ADIN23	Cyclic Trim Actuator Position (Forward and Aft)
ADIN24	Rotor RPM
ADIN25	Spare
ADIN26	APU Accumulator Pressure
ADIN27	Fuel Quantity - Left & Right Forward Auxilliary Tanks
ADIN28	Fuel Quantity - Left & Right Aft Aux. Tanks
ADIN29	Fuel Quantity - Left & Right Main Tanks
ADIN30	Generator 1 & 2 Load
ADIN31	Rectifier 1 & 2 Load

TABLE 3.1.3(b)

TEST PANEL INPUT	CONTROLLED PARAMETER
DIN00-13	Spares
DIN14	Eng 1 Start Fuel
DIN15	Eng 2 Start Fuel
DIN16-19	Spares
DIN20	Eng 1 Oil Low
DIN21	Eng 2 Oil Low
DIN22	Eng 1 Chip
DIN23	Eng 2 Chip
DIN24	Eng 1 Condition Lever - Ground
DIN25	Eng 1 Condition Lever - Fly
DIN26	Eng 2 Condition Lever - Ground
DIN27	Eng 2 Condition Lever - Fly
DIN28	Eng 1 Ignition
DIN29	Eng 2 Ignition
DIN30	Eng 1 Starter
DIN31	Eng 2 Starter
DIN32	Acknowledge
DIN33	Faults Enable
DIN34	Ground Contact
DIN35-55	Spares

TABLE 3.1.3(b) con'td 7.2

TITLE: Bus Controller Functional Verification

<u>PURPOSE</u>: Demonstrate the capability of the EMMADS 701-A to act as a bus controller.

HARDWARE CONFIGURATION:

- 1) Data Bus/Monitor Controller (DBMC)
- 2) Remote Transmission Unit Emulator (RTU-EM)
- 3) EMMADS Raster Symbol Generator (ERSG)
- 4) System Test Panel
- 5) Terminet

SOFTWARE REQUIRED:

- 1) Software to cause DBMC to act as a bus monitor
- 2) Emulator operational program
- 3) EMMADS operational program

STIMULI: Various test panel pots and switches. (See Fig. 3.1.3)

TEST PROCEDURE:

- 1) Power up the ERSG, the RTU-EM , the DBMC and the system test panel
- 2) Observe bus traffic on the DBMC

ACCEPTANCE CRITERIA: Observe that the data transferred on the bus is similar to that displayed in test 3.1.3. Data can be placed on the bus from the test panel and varied in accordance with Table 3.1.3(a), Dump ERSG bus controller memory and note that it is the same as that displayed on the DBMC.

TEST HISTORY:

DATE 3.20 d/ STATUS D WITNESS WITNESS

TITLE: Symbol Generator Functional Verification

<u>PURPOSE</u>: Demonstrate the capability of the EMMADS raster symbol generator to display on a solid state display panel alphanumeric graphic and block filled symbols.

HARDWARE CONFIGURATION:

- 1) Remote Transmission Unit Emulator (RTU-EM)
- 2) EMMADS Raster Symbol Generator (ERSG)
- 3) Solid State Display Panel
- 4) System Test Panel
- 5) Relegendable Switches

SOFTWARE REQUIRED:

- 1) Emulator operational program
- 2) EMMADS operational program

STIMULI: Various test panel pots and switches. (See Fig. 3.1.3)

TEST PROCEDURE:

- 1) Power up the RTU-EM, the ERSG, the system test set and the solid state display.
- 2) Place fault enable switch (DIN-33) in "OFF" position.
- 3) Press display panel switch marked "ENG DATA".
- 4) Adjust Eng. 1 oil pressure pot (ADIN-06) until the display reads "110".
- 5) Place fault enable switch in "ON" position.

ACCEPTANCE CRITERIA: Observe that alphaumeric, graphic and block filled symbols are displayed on the solid state display panel.

TEST HISTORY:

DATE 3-20-5/ STATUS CL WITNESS & VE

TITLE: Relegendable Switch Functional Verification

PURPOSE: Demonstrate the capability to relegend the switches under the display.

HARDWARE CONFIGURATION:

- 1) Remote Transmission Unit Emulator (RTU-EM)
- 2) EMMADS Raster Symbol Generator (ERSG)
- 3) Solid State Display Panel
- 4) System Test Panel
- 5) Relegendable Switches

SOFTWARE REQUIRED:

- 1) Emulator operational program
- 2) EMMADS operational program

STIMULI: Relegendable switches under display panel.

TEST PROCEDURE:

- 1) Power up the RTU-EM, the ERSG, the system test set.
- 2) Press display panel switch marked "ENG DATA".

ACCEPTANCE CRITERIA: Observe that the switch legend changes to "ENG PROC" and that the switch marked "CHCK LSTS" becomes "STAT ONLY".

TEST HISTORY:

DATE 3-20-81 STATUS CL WITNESS ZU

TITLE: Key Pad Functional Verification

<u>PURPOSE</u>: Demonstrate that the Termiflex keypad and the EMMADS raster symbol generator can exchange information.

HARDWARE CONFIGURATION:

- 1) Remote Transmission Unit Emulator (RTU-EM)
- 2) EMMADS Raster Symbol Generator (ERSG)
- 3) System Test Panel
- 4) Termiflex keypad

SOFTWARE REQUIRED:

- 1) Emulator operational program
- 2) EMMADS operational program

STIMULI: Key entry from Termiflex

TEST PROCEDURES:

- 1) Power up the RTU-EM, the ERSG, the system test panel and the Termiflex keypad.
- 2) Place the Termiflex in the "ON LINE" mode.
- 3) Make a key entry on Termiflex.

ACCEPTANCE CRITERIA: Observe that the character entered from the Termiflex keypad appears on it's integral display.

TEST HISTORY:

DATE 3-20-81 STATUS C. WITNESS PUR

TITLE: Normal Operation: Acknowledge Switch Function

<u>PURPOSE</u>: Verify proper operation of the acknowledge switch when no faults are detected.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Applicable test panel switch. (See Fig. 3.1.3)

INSTRUMENTATION: None

TEST PROCEDURE:

- 1. Power up system in normal configuration
- 2. Place test panel discrete input (DIN) switch 32 in the ON position and note display reaction.
- 3. Place DIN 32 in the OFF position and note display reaction.

ACCEPTANCE CRITERIA: Switch movement should have no affect on the display.

TEST HISTORY:

DATE 3-20-81 STATUS CX WITNESS VIV

TITLE: Normal Operation: Relegendable Switch Function.

<u>PURPOSE</u>: Verify proper correlation between switch actuation, switch legend changes and display formats.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REOUIRED: Operational programs for EMMADS and Emulator.

STIMULI: None.

INSTRUMENTATION: None.

TEST PROCEDURES:

1. Power up system in normal configuration.

- 2. Note display switch legends and display format and compare with Fig. 3.2.1.2(a).
- 3. Depress display panel switch labeled "ENG DATA" and note resulting switch legends and display format.

 Compare to Fig. 3.2.1.2(b).
- 4. Depress display panel switch labeled "ENG PROC" five times and note resulting switch legends and display formats.

 Compare with Figs. 3.2.1.2 (c), (d), (e), (f) and (a) in that order.
- 5. Depress display panel switch labeled "FUEL DATA" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(g). Depress display panel switch labeled "FUEL PROC" and compare resulting switch legends and display with Fig. 3.2.1.2(a).
- 6. Depress display panel switch labeled "XMSN DATA" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(h). Depress display panel switch labeled "XMSN PROC" and compare resulting switch legends and display with Fig. 3.2.1.2(a).
- 7. Depress display panel switch labeled "ELEC DATA" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(i). Depress display panel switch labeled "ELEC PROC" and compare resulting switch legends and display with Fig. 3.2.1.2(a).

TEST PROCEDURES:

- 8. Depress display panel switch labeled "HYDR DATA" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(j). Depress display panel switch labeled "HYDR PROC" and compare resulting switch legends and display with Fig. 3.2.1.2(a).
- 9. Depress display panel switch labeled "MISC DATA" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(k). Depress display panel switch labeled "MISC PROC" and compare resulting switch legends and display with Fig. 3.2.1.2(a).
- 10. Depress display panel switch labeled "CHCK LSTS" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(1).
- 11. Depress display panel switches labeled "EMER STRT", "NORM STRT", "T/O CHCK", "CRSE CHCK", LNDG CHCK" and "SHUT DOWN" one at a time and in each case note the resulting condition of the display and the switch legends (there should be no change). Compare to those of Fig. 3.2.1.2(1).
- 12. Depress display panel switch labeled "PERF CALC" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(m).
- 13. Depress display panel switches labeled "HIT CHCK", "HVR PWR", "HVR WT", 'MAX PWR", "WT & BAL" and "SYST TEST" one at a time and in each case note the resulting condition of the display and the switch legends (there should be no change). Compare to those of Fig. 3.2.1.2(m).
- 14. Depress display panel switch labeled "STAT ONLY" and note resulting switch legends and display format. Compare with Fig. 3.2.1.2(a).

ACCEPTANCE CRITERIA: The switch legends shall be in accordance with the figures referenced in the applicable Test Procedures item. The display format changes observed shall also be in accordance with said figures although the actual numeric values and scale indicator positions may vary from those in the figures.

TEST HISTORY:

DATE 3-20-81 STATUS C4 WITNESS PIL

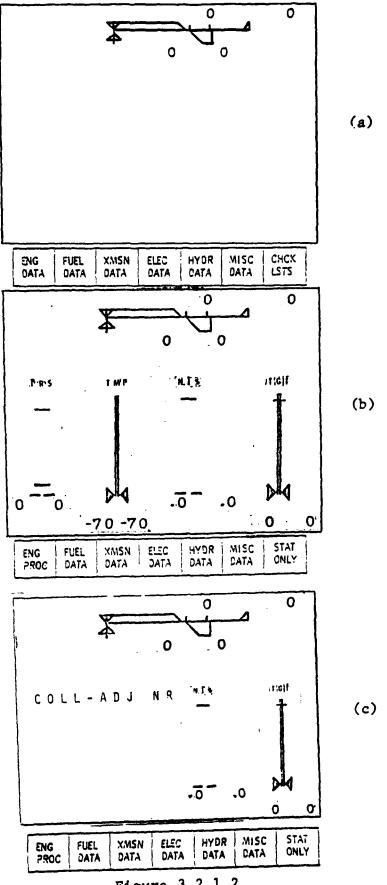


Figure 3.2.1.2 16

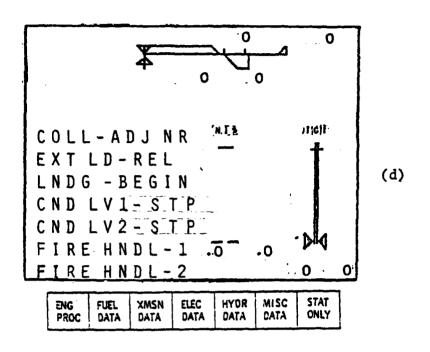
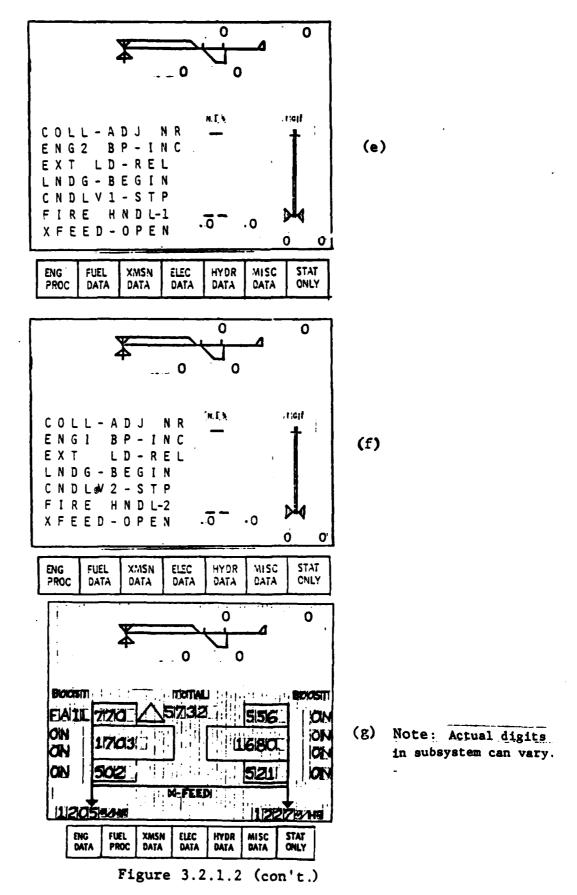


Figure 3.2.1.2 (cont)
16.1



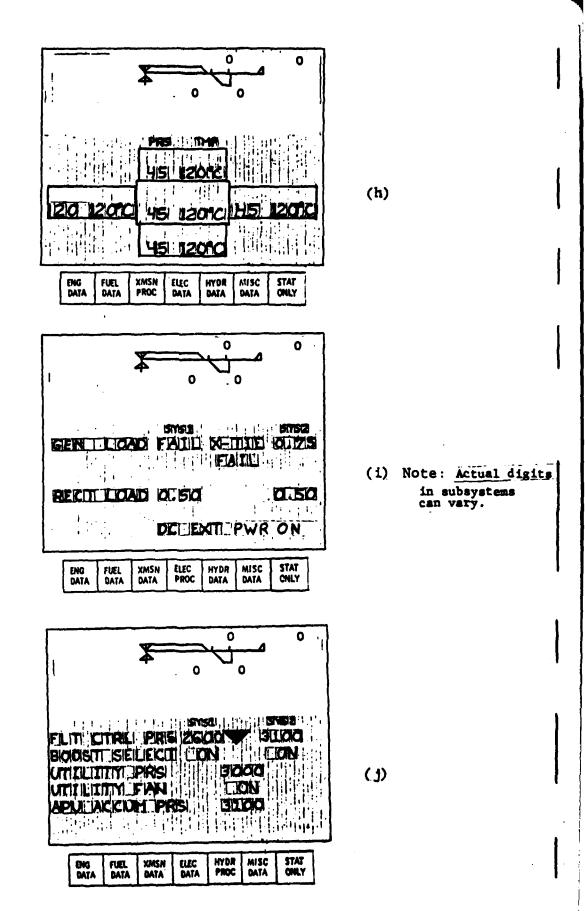


Figure 3.2.1.2 (con't.)

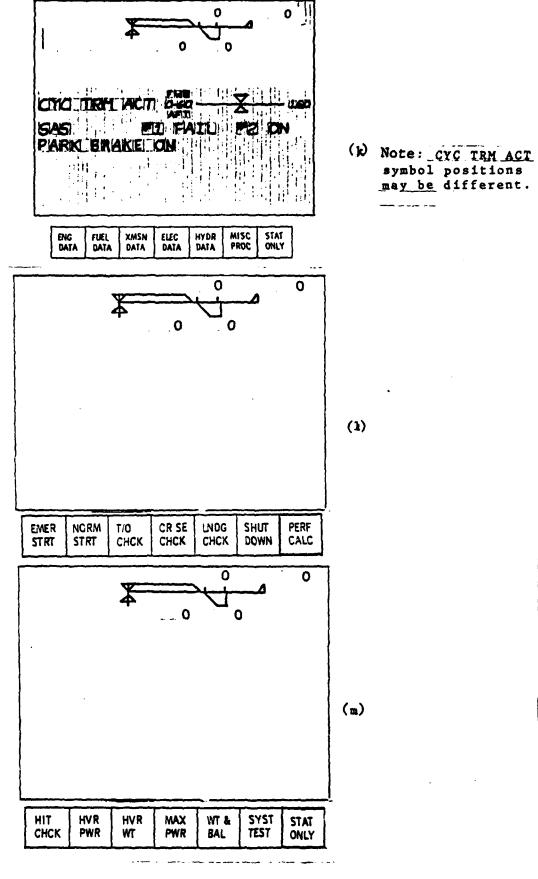


Figure 3.2.1.2 (con't) 19

TITLE: Normal Operation: Display Functions - Analog Signal Range

<u>PURPOSE</u>: Verify the capability to drive all displayed analog indicators through their full dynamic range using related test panel analog inputs.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Inputs from system test panel. (See Fig. 3.1.3)

INSTRUMENTATION: None.

TEST PROCEDURE:

- 1. Power up system in normal configuration.
- 2. Depress panel switch marked "ENG DATA".
- 3. For each analog scale being checked verify full dynamic range available by turning the related test panel potentiometer and reading the signal value from the digital readout associated with the scale. Observe indicator movement over entire length of scale.

ACCEPTANCE CRITERIA: Display performance of tested signals shall meet the criteria set forth in Table 3.2.1.3.1.

TEST HISTORY:

DATE 3-20-81 STATUS C WITNESS YCK

Table 3.2.1.3.1

TITLE: Normal Operation: Display Functions - Analog Scale Range
Delimiters

<u>PURPOSE</u>: Verify proper position and turn-on/turn-off thresholds of scale delimiters.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational Programs for EMMADS and Emulator

STIMULI: Inputs from system test panel. (See Fig. 3.1.3)

INSTRUMENTATION: None

TEST PROCEDURE:

1. Power up system in normal configuration.

- Depress display panel switch marked "ENG DATA".
- 3. Delimiter positions and turn-on/turn-off thresholds are tested using the Test Procedure Data portion of Table 3.2.1.3.2 in the following manner:
 - a. Under the Test Parameter column, locate the display scale name (on which the delimiter resides) and the test panel scale indicator control.
 - b. For each delimiter being tested, set all test panel A/D inputs (ADIN's) to zero (using the display readouts) and all discrete inputs (DIN's) low (OFF). Then establish any special starting conditions as shown by the table.
 - c. Beginning from the initial conditions, increase the Control Parameter to its maximum, observing the values of the Control Parameter when the delimiter turns on and off. Repeat this while decreasing the Control Parameter to its initial condition. The delimiter position is obtained by adjusting the Test Parameter scale indicator to coincide with the delimiter symbol and reading the scale's digital value.

TEST PROCEDURE: (Con't.)

- 3. d. For the first four parameters, conditions and parameter information is shown for the delimiter on the scale for either engine. For the NR (Rotor) and Torque scales, use of the word "OR" in the Special Conditions and Control Parameter columns indicates the tests should be run with each condition/parameter group separately. Use of the word "AND" indicates joint conditions/parameter adjustment should also be tested.
 - e. Note that there is a special torque delimiter test which is based on a fixed torque value (ENG 1 or 2) and controlled by varying Rotor RPM (NR) from 230 to 245.

ACCEPTANCE CRITERIA: Delimiter positions and turn-on/turn-off thresholds shall agree with those shown in the Acceptance Criteria Data section of Table 3.2.1.3.2.

TEST HISTORY:

DATE 3-20-87 STATUS CK WITNESS PULL

TEST PANEL SPECIAL CONDITIONS NAME TEST PANEL DELIMITER DE			TEST PROCEDURE DATA-				ACCE	TANCE CR	ACCEPTANCE CRITERIA DATA-	\TA	
ADINOS ADINOS ADINOS ADINOS ADINOS ADINOS ADINOS ADINOS BNOTE BNOTE SENCI N2 ADINOS ADINOS BNOTE BNOTE SENCI N2 BNOTE BNOTE SENCI N1 BNOTE BNOTE SENCI N1 BNOTE BNOTE SENCI STATE, ST FUELELIGNICION BNOTE B	r PARA	METER TEST PANEL INPUT	SPECIAL CONDITIONS	CONTROL PAR	ANEL	DELIMITER SYMBOL	DELIMITER	INCRE TURN ON	THRESHOLDS INCREASING DI TURN ON TURN OFF TURN	OLDS DECRE TURN ON	OLDS DECREASING TURN ON TURN OFF
ADINO? ADINOB ADINOB ADINOB ADINOB ADINOB ADINOB BENGI DIN 14, 28, 30 = ENGI LOR DIN 26, 25 ADINOD BENGI DIN 14, 28, 30 = ENGI LOR DIN 26, 25 ADINOD BENGI DIN 14, 28, 30 = ENGI LOR DIN 26, 25 ADINOD BENGI DIN 24, 25,0,0 BENGI DIN 24, 25,0,0 BENGI TGT ADINO2 BENGI DIN 24, 25,0,0 BENGI TGT ADINO3 BENGI DIN 24, 25,0,0 BENGI TGT ADINO3 BENGI DIN 24, 25,0,0 BENGI TGT ADINO3 BENGI DIN 26, 27,0,0 BENGI TGT ADINO3 BENGI TGT ADINO3 BENGI DIN 26, 27,0,0 BENGI TGT ADINO3 BENGI TGT ADINO3 BENGI TGT ADINO3							20+2	0	70+.5	70±.5	N/A
ADINOS ADINOS None ENGI NZ ADINOS None ENGI NZ ENGI Start, St Fueldignition ENGI DIN 14,28,30 = ENGI Cond LVR ADINOS ENGI DIN 14,28,30 = ENGI Cond LVR ADINOS ENGI DIN 15,29,31 = ENGI NI 1,1,1 ENGZ Cond LVR ENGI NI ENGZ DIN 26,25 ENGI TGT ADINOS ENGI DIN 24,25,0,0 ENGI TGT ADINOS ENGI DIN 24,25,0,0 ENGI TGT ADINOS ENGI DIN 26,27,0,0 ENGI TGT ADINOS ENGI TGT	Oil Pressure	Aprilos		77	CONTRA	1	35 <u>+</u> 1	70±.5	95±.5	5.±26	70±.5
ADINO8 ADINO9 None None ENGI Start, St DIN 30,14, Fueldignition ENGI DIN 14,28,30 - ENGI Cond LVR DIN 24,25 ADINO1 ENGI DIN 15,29,31 - ENGI Cond LVR DIN 26,27 ADINO2 ENGI DIN 24,25,00,0 ENGI TGT ADINO3 ENGI DIN 24,25,00,0 ENGI TGT ADINO3	(Ewa)	ADINO?	None	ENGI MI	DONITOR		50 <u>+</u> 1	704.5	N/A	N/A	75+.5
ADIMOS ADIMOS None RIGI Start, St Fuel&Engrition ENGI DIN 14, 28, 30 = ENGI Cond LVR ADINO ENGI DIN 15, 29, 31 = ENGI Cond LVR I, 1, 1 ADINO2 ENGI DIN 24, 25, 0, 0 ENGI TGT ADINO3 ENGI DIN 24, 25, 0, 0 ENGI TGT ADINO3 ENGI DIN 24, 25, 0, 0 ENGI TGT ADINO3 ENGI DIN 24, 25, 0, 0 ENGI TGT ADINO3	(EMS2)			ENGT NZ	TOUTON		90+1	95±.5	N/A	N/A	95+.5
ADIMOS ADIMOS None ENGI Start, St Fuel&ignition ENGI DIN 14, 28, 30 - ENGI Cond LVR ADINO1 ENGI DIN 14, 28, 30 - ENGI Cond LVR I, 1, 1 I, 1, 1 ENGI DIN 24, 25, 0, 0 ENGI NI ADINO2 ADINO2 ADINO3 ENGI DIN 24, 25, 0, 0 ENGI TGT ADINO3 ENGI DIN 24, 25, 0, 0 ENGI TGT ADINO3 ENGI DIN 24, 25, 0, 0 ENGI TGT ADINO3							110+2	Contin	N/A	Contin	N/N
ADINO2 ENGI DIN 24,25,00 ENGI TGT ADINO3 ADINO3 ENGI DIN 24,25,00 ENGI TGT ADINO3 ADINO3 ENGI DIN 24,25,00 ENGI TGT ADINO3 ENGI DIN 24,25,00 ENGI TGT ADINO3	Oil Temp (ENG) (ENG2)	ADINO8 ADINO9	None	None	N/A		138±2	Contin	N/A	Contin	N/A
ADINO0 ENG1 DIN 14,28,30 = ENG1 Cond LVR DIN 24,25 ADINO1 ENG2 DIN 15,29,31 = 1,1,1 I,1,1 ADINO2 ENG1 DIN 24,25≠0,0 ENG1 N1 ADINO2 ENG1 DIN 24,25≠0,0 ENG1 TGT ADINO3 ENG2 DIN 26,27≠0,0 ENG2 TGT ADINO3				ENGI Start, St Fuel&Ignition ENG2 Start, St Fuel&Ignition	DIN		1 - 54	1,1,1	N/A	N/A	1,1,1
ADINO1 ENG2 DIN 15, 29, 31 - ADINO2 ENG1 DIN 26, 27 - ADINO3 ENG2 DIN 26, 27 +0, 0 ENG1 TGT ADINO3 ADINO3 ENG2 DIN 26, 25 +0, 0 ENG2 TGT ADINO3 ADINO3 ENG2 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ADINO3 ENG2 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG2 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG2 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG2 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT ADINO3 ENG3 DIN 26, 27 +0, 0 ENG3 TGT	las Producer (ENGI)	ADIN00	,28,30	ENG1 Cond LVR	DIN 24,25		1 - 09	1,0	W/A	N/A	1,0
ADINO2 ENG1 DIN 24,25\$0,0 ENG1 TGT ADINO3	(ENC2)	ADIN01	un' i	ENG2 Cond LVR	DIN 26,27		1 - 59	0,1 or 1,1	N/A	N/A	0,0 or 1,0
ADINO2 ENG1 DIN 24,25\$0,0 ENG1 TGT ADINO2	,		None		ADINO0 ADINO1		1 - 801	Contin	N/A	Contin	N/A
ADINO2 ENG1 DIN 24,25\$0,0 ENG1 TGT ADINO2 ADINO3 ENG2 DIN 26,27\$0,0 ENG2 TGT ADINO3							400+20	0	770±5	765±5	N/A
ADINO3 ENG2 DIN 26,27#0,0 ENG2 TGT ADINO3	emperature				-		5+011	0	810±5	805±5	N/A
ADINOS EMEZ DIN 20,2770,0 EMEZ 161 ADINOS	(EMC1)	ADIMOS	ENG1 DIN 24,2370,0		AUTHOS		5∓018	770±5	5 ∓098	855±5	57591
	(2007)	SOLINO.	C. U. CO, C. 170, U		ADIMO		5∓098	810±5	V/N	N/A	805±5
					1	1	927±5	Contin	N/A	Contin	V/N

딾	TEST PROCEDURE DATA	CONTROL PAI			ACCEP	TANCE CR	ACCEPTANCE CRITERIA DATA—		
SPECIAL CONDITIONS	·s	NAME	TEST PANEL. INPUT	SYMBOL.	DELIMITER DELIMITER SYMBOL POSITION	TURN ON	INCREASING TURN ON TURN OFF		DECREASING TURN ON TURN OFF
					0 <u>+</u> 5	Contin	N/A	Contin	N/A
				/	232±1	Contin	N/A	Contin	N/A
2				1	235±1	Contin	N/A	Contin	N/A
2100					245+1	Contin	N/A	Contin	N/A
		a z	APIN26		261±1	Contin	N/A	Contin	N/A
			171100	Γ	765±1	Contin	N/A	Contin	N/A
ADINOVACA TRNC				L	250±1	Contin	N/A	Contin	N/A
ADINOS-41 (ENG! 114)				7	14552	250+1	N/A	17677	N/A
(htt zona) th-Conta					1+797	Contin	N/A	Contin	N/A
ADINO0>65 (ENG1 NI)6 DIN24, 25.34=0, 1, 1 OR ADINO1>65 (ENG2 NI)6 DIN26, 27.34=0, 1, 1				Y	214±1	0	232±1	230 <u>+</u> 1	N/A
ENG!	S	l and/or 2	ADTMO//05		78+1	Contin	N/A	Contin	N/A
(anhioi) the control of		Torque	CO / LOWING		1001	Contin	N/A	Contin	N/A
					85 <u>+</u> 1	Contin	85±1	84±1	N/A
oto				7	97+1 (toI00)	85±1	N/A	N/A	84+1
					138+1	Contin	N/A	Contin	N/A
)	₹-68	Contin	89-1	88 [‡] 1	N/A
(Rotor)		ENG 1 or 2	ADINO4or05	7	14001	1+68	N/A	N/A	88±1
OF U1 = 73(M1)		anbior			138+1	Contin	N/A	Contin	N/A
					91+1	Contin	91±1	1-06	N/A
					1001	91 <u>+</u> 1	N/A	N/A	90+1
(TE) C/ = 10 OO OO OO					138+1	Contin	N/A	Contin	N/A
ADIN24=230(NR) ADIN04 or 05=92 ADIN00 or 01 = 75(N1)	}	NR	ADIN24	- •	This is a below the delimiter on the tor to 245 the bar which	a speciale scale ir is a sortue scordue sche line ih runs f	scale line. At 2 is a short vertification scale. As NR que scale. As NR ine expands he runs from 97+1 t	a delimiter j 230-235 NR the tical line at l NR increases fr norizontally in to 100+1 when	This is a special test on a delimiter just below the scale line. At 230-235 NR the delimiter is a short vertical line at 100+1 on the torque scale. As NR increases from 235 to 245 the line expands horizontally into a bar which runs from 97+1 to 100+1 when NR=245

<u>TITLE</u>: Normal Operation: Display Functions - Remaining Flight Time Clock

<u>PURPOSE</u>: Demonstrate the functional dynamic range of the Remaining Flight Time Clock.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Inputs from system test panel. (See Fig. 3.1.3)

TEST PROCEDURE:

- 1. Power up system in normal configuration.
- 2. Rotate test panel A/D inputs (ADIN) pots 27, 28 and 29 (fuel quantity in the forward, aft and main tanks) fully clockwise, simulating full fuel load. Note the indicated remaining flight time in minutes in the upper right hand display corner.
- 3. Slowly rotate each pot fully counter-clockwise and note behavior of clock.

ACCEPTANCE CRITERIA: When a full fuel load is simulated remaining flight time shall be 195 ± 1 minute. The clock time shall decrease as fuel quantity decreases and remain stable to within ± 1 minute when pots are stationary. The clock time shall indicate zero when all pots are rotated fully counter clockwise.

TEST HISTORY:

DATE 3-20-87 STATUS OF WITNESS LIKE

TITLE: Fault Detection: Acknowledge Switch Function

<u>PURPOSE</u>: Verify proper ackowledge switch function when faults are detected.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Inputs from system test panel. (See Fig. 3.1.3)

INSTRUMENTATION: None

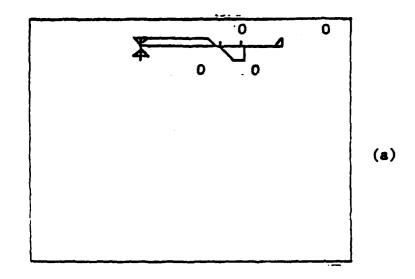
TEST PROCEDURE:

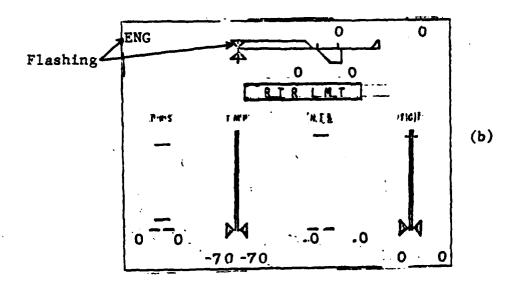
- 1. Power up system in normal configuration, note display condition and compare with Fig. 3.2.2.1 (a).
- 2. Place discrete test panel input (DIN) switch 33 (Fault Enable) in the up (ON) position, note display conditions and compare with Fig. 3.2.2.1 (b).
- 3. Place DIN 32 in the ON position and then return to OFF, noting display in both cases. Compare with Fig. 3.2.2.1 (c).
- 4. Repeat Step 3 and compare display with Fig. 3.2.2.1 (d).

ACCEPTANCE CRITERIA: The display formats shall match those in Fig. 3.2.2.1 as referenced in each step of the test procedure.

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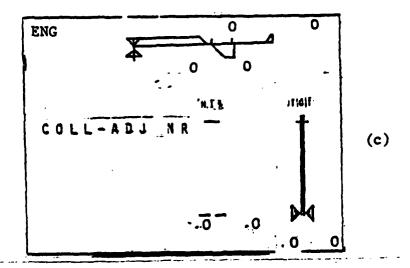


Figure 3.2.2.1

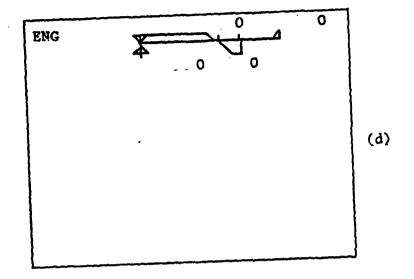


Figure 3.2.2.1 (cont) 28.1

TITLE: Fault Detection: Relegendable Switch Function

PURPOSE: Verify proper operation of relegendable switches when

faults are detected.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Inputs from system test panel (See Fig. 3.1.3)

INSTRUMENTATION: None

TEST PROCEDURE:

1. Power up system in normal configuration, note display condition and switch legends and compare with Fig. 3.2.2.2(a).

- 2. Place discrete test panel input (DIN) switch 33 (Fault Enable) ON and compare display and switch legends with Fig. 3.2.2.2(b). Depress display panel switches labeled "FUEL DATA" "XMSN DATA", "ELEC DATA", HYDR DATA", and MISC DATA" in succession. Compare display condition and switch legends with Fig. 3.2.2.2(b).
- 3. Depress display panel switch labeled "ENG ACK" twice and compare display and switch legends with Figs. 3.2.2.2 (c) & (d) respectively.
- 4. Place DIN 33 OFF. Now compare display and switch legends to Fig. 3.2.2.2(a). Place DIN 25 ON and then DIN 33 also. Compare switch legends and display to Fig. 3.2.2.2(e).
- 5. Depress display panel switch labeled "WARN ACK" and compare display and switch legends with Fig. 3.2.2.2(d).
- 6. Depress display panel switch labeled "ENG DATA" and then "ENG PROC". Compare display and switch legends with Fig. 3.2.2.2(f). Depress the "ENG ACK" switch twice and compare the display and switch legends to Figs. 3.2.2.2(g) & (d) respectively.

ACCEPTANCE CRITERIA: The display formats and switch legends shall match those in Fig. 3.2.2.2 as referenced in each step of the test procedure.

TEST HISTORY:			
	TFCT	HICTORY	

DATE 3.16 F/ STATUS OK WITNESS T

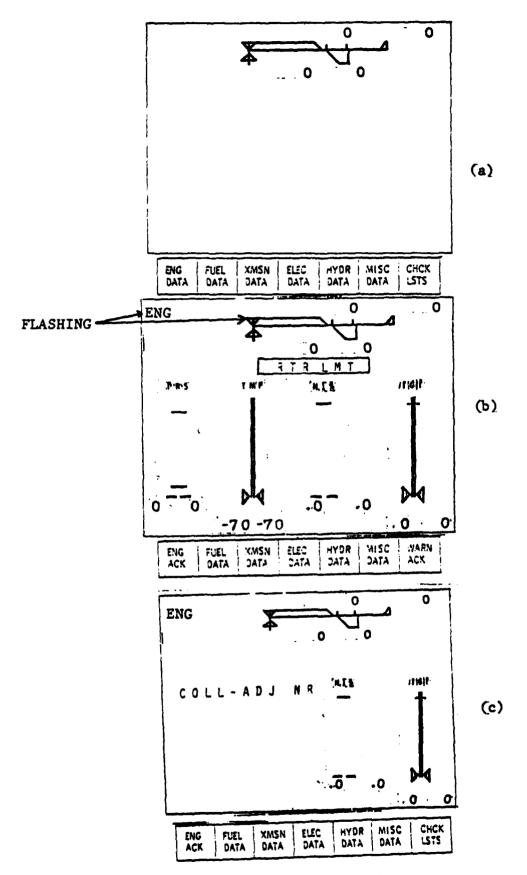


Figure 3.2.2.2

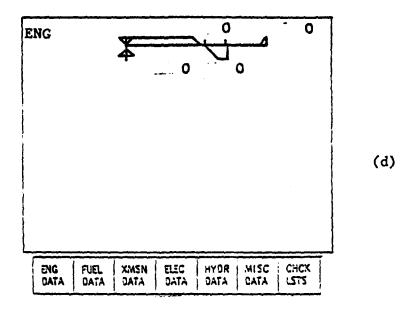


Figure 3.2.2.2 (cont) 30.1

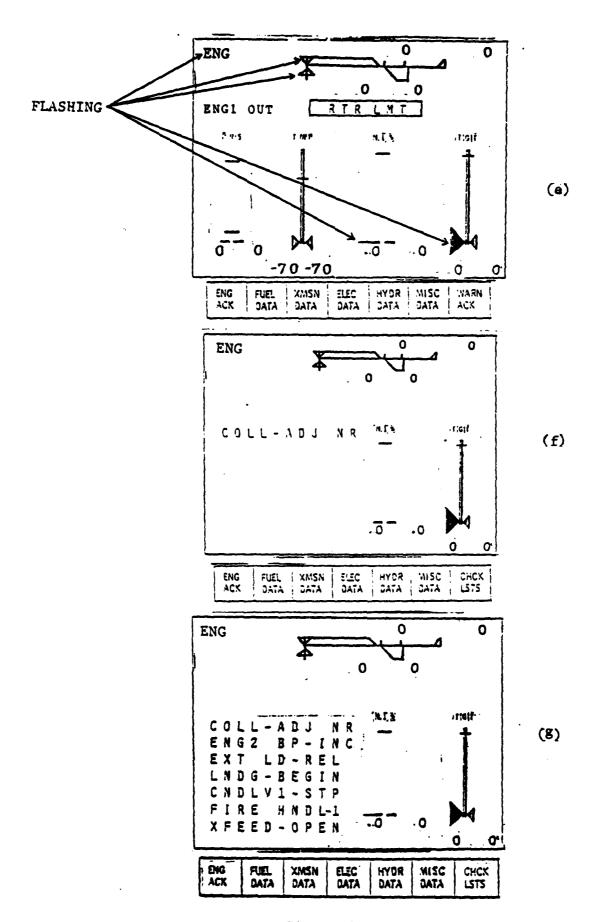


Figure 3.2.2.2 (con't)

TITLE: Fault Detection: Display Functions - Discrete Single

Parameter Faults

PURPOSE: Verify proper display function when discrete single parameter faults are input to the system.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational Programs for EMMADS and Emulator

STIMULI: Test panel discrete inputs (See Fig. 3.1.3)

INSTRUMENTATION: None

TEST PROCEDURE:

- 1. Power up system in normal configuration and note the "No Fault" display condition. Compare with Fig. 3.2.2.3.1 (a).
- 2. Place DIN 33 (Fault Enable) in the ON position. Depress display panel switch labeled "WARN ACK".
- 3. Verification of the six discrete inputs (single parameter faults) is accomplished by reference to Table 3.2.2.3.1 as follows:
 - a. Setup initial conditions
 - b. Set the discrete fault input as shown.
 - c. Compare display condition with the indicated figure.
- 4. At the completion of each test, use the acknowledge switch (DIN 32) to reset the display

ACCEPTANCE CRITERIA: The display shall reflect the condition shown in the figures referenced for each test in Table 3.2.2.3.1 and as referenced above for other conditions.

TEST HISTORY:

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Table 3.2.2.3.1

Initial Conditions	Discrete Input	Figure 3.2.2.3.1
None	DIN 22 = 1 (Eng 1 Chip)	(b)
None	DIN 23 = 1 (Eng 2 Chip)	(c)
None	DIN 20 = 1 (Eng 1 Oil Low)	(d)
None	DIN 21 = 1 (Eng 2 Oil Low)	(e)
ADIN 04 = 1% (Eng 1 Torque)	DIN 24 & 25 = 1,1 (Eng 1 Cond. Lever Out of Detent)	(f)
ADIN 05 = 1% (Eng 2 Torque)	DIN 26 & 27 = 1,1 (Eng 2 Cond. Lever Out of Detent)	(g)

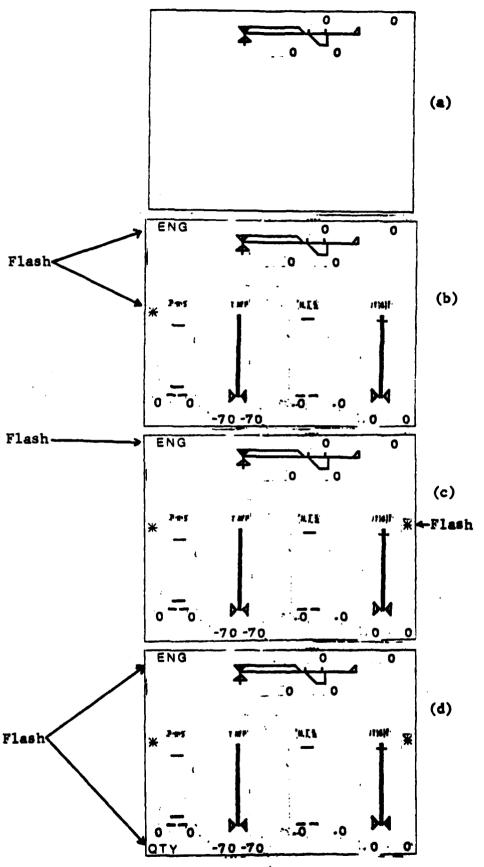
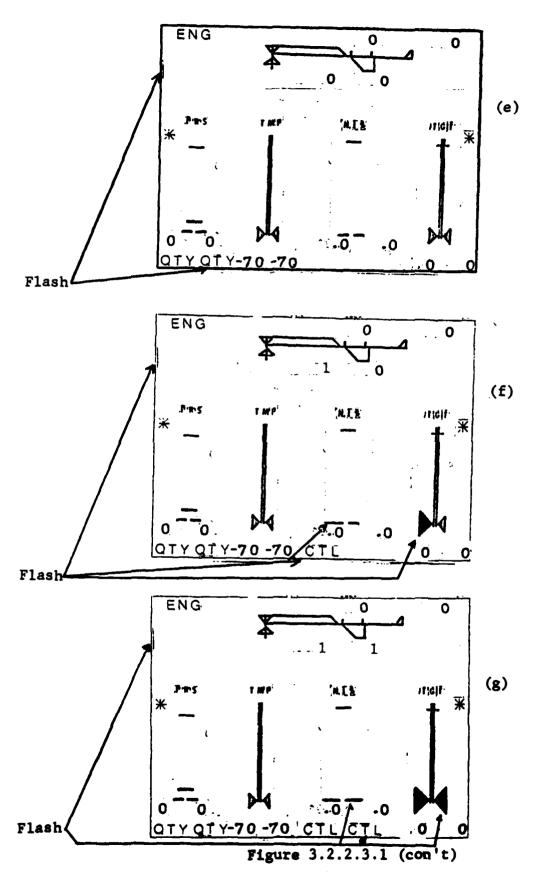


Figure 3.2.2.3.1



<u>TITLE</u>: Fault Detection: Display Functions - Analog Single Parameter Faults.

<u>PURPOSE</u>: Verify proper display function when analog single parameter faults are input to the system.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Test panel analog and discrete inputs. (See Fig. 3.1.3)

INSTRUMENTATION: None

- 1. Power up system in normal configuration and compare the display condition with Fig. 3.2.2.3.1 (a).
- 2. Verification of analog single parameter fault algorithms proceeds as follows, using Table 3.2.2.3.2:
 - a. Prior to each test, display the engine subsystem using the "ENG DATA" display panel switch and set the analog portion of the Initial Conditions. (Include ADIN 24 = 240 rotor RPM for all except the rotor faults.)
 - b. Use the "STAT ONLY" display panel switch to remove the engine subsystem from the display.
 - c. Set the remaining discrete Initial Conditions and enable the faults by placing the discrete input (DIN) 33 in the ON position. Acknowledge any faults.
 - d. Slowly increase or decrease the Test Parameter from its initial condition, as indicated in the Table.
 - e. Note the value of the Test Parameter when the display indicates the fault condition has occurred.
 - f. When an initial condition is given as <, >, < or > with respect to certain limits, verify the fault exists within the indicated range(s) by varying the parameter(s) over all such values.
- 3. At the completion of each test, disable all faults by placing DIN 33 in the OFF position.

TEST NUMBER: 3.2.2.3.2 (con't.)

ACCEPTANCE CRITERIA: The test parameter value when a fault condition trips shall match that contained in the table. The analog scale indicator for the parameter under test shall appear oversized, filled in, and shall flash when the fault condition is first detected/displayed except for the torque and rotor symbols which will only flash. Also the word "ENG" shall appear in the upper left display corner and shall flash when the fault is detected, as shown in Fig. 3.2.2.3.1(b). In addition the rotor RPM faults shall display a message capsule as shown in Fig. 3.2.2.2(b).

TEST HISTORY:

DATE 3-20-87 STATUS CX WITNESS DIC

TEST PAR		ì	ì	TEST PARAM	ETER
NAME	TEST PANEL INPUT	INITIAL CONDITIONS	INCREASE	DECREASE	FAULT THRESHOLD
		ADIN 06=30 (ENG1 0il Press) ADIN 00<45(ENG1 N1)		х	None
		ADIN 06-30 45 <adin00<70< td=""><td></td><td>x</td><td>20<u>+</u>1</td></adin00<70<>		x	20 <u>+</u> 1
Engine 1 Oil		ADIN06-40		Х	35 <u>+</u> 1
Pressure	ADINO6	70 <u><</u> ADINO0<95	Х		50 <u>+</u> 1
		ADIN06=70		Х	50 <u>+</u> 1
		ADIN00>95	Х		90 <u>+</u> 1
		ADINO6-0	Х		110 <u>+</u> 1
Engine 2 Oil Pressure	ADINO7	(Same test as above, substituting ADINO1 for ADINO6)	(ENG 2 N1)	for ADINO	0, and ADINO7
Engine 1 Oil Temperature	ADIN08	ADINO8=0	х		138 <u>+</u> 1
Engine 2 0il Temperature	ADIN09	ADINO9-0	х		138 <u>+</u> 1
		ADINO4 > 5 (ENGL Torque)		х	60 <u>+</u> .5
Engine 1 Gas Producer (N1)	ADIN00	ADINO6 > 20 (ENG1 Oil Press) ADINO0 = 62 DIN24 = 1 (ENG1 Cond Lever - Ground)	х		63±.5
,		ADING4 > 5, ADING6 > 20 ADING0=68, DIN25=1(ENG1 Cond Lever-Flight		х	65 <u>+</u> .5
		DIN24,25=0,1 (ENG1 Cond. Lever-FLIGHT) ADIN04>5,50< ADIN06<90, ADIN00=98 (Note: The fault should not be disabled by changing the states of DIN 24+25)	х		103 <u>+</u> .5
Engine 2 Gas Producer (N1)	ADIN01	(Same tests as above, substituting ADIN (DIN 26.27 (ENG2 Cond. Lever) for DIN 24	05+07 for 8 .25	DIN 04+06	respectively and
		ADIN02=0 DIN14,28,30=1,1,1(ENG1 Start Fuel, Ignition and Starter)	Х		788 <u>+</u> 1
		ADINO2=0	X		260 <u>+</u> 1
			Х		350 <u>+</u> 1
ENG1 Turbine	ADINO2	ADIN02=450. ADIN00=75, 35 < ADIN06 < 50 DIN24,25 ≠ 0,0 (ENG1 Cond Lever not in		X	399 <u>+</u> 1
Gas Temp		STOP) ADINO2=780 (Rest same as above)	Х	· ·	770 <u>+</u> 1
		DIN24.25#0.0 ADIN02=840 (Rest same as above)	Х		810 <u>+</u> 1
		DIN24,25#0,0	х		860 <u>+</u> 1
		ADIN02=870	Х		927 <u>+</u> 1
ENG2 Turbine Gas Temp	ADINO3	(Same test as above, substituting DIN 15, 25 respectively. Also ADIN 01+07 for ADI	29,31,26 & N 00+06 rea	27 for DI spectively	N 14,28,30,24&
		ADIN04=50 ADIN05=50	х		78 <u>+</u> 1
		ADIN04-80 (Do not confuse with 10 second ADIN05-50 rime out fault)	x		100+1
		ADIN04=50 ADIN06=40 ADIN24>245 (Rotor RPM) ADIN00=75	x		85 <u>+</u> 1
		ADIN04=85 ADIN06=40 ADIN24=240 ADIN00=75	X		87 <u>+</u> 1
Engine 1	ADINO4	ADIN04=87 ADIN06=40 ADIN24=235 ADIN00=75	x		89 <u>+</u> 1
Torque	AD INO-	ADIN04-89 ADIN06-40 ADIN24<230 ADIN00-75	x		91 <u>+</u> 1
		ADIN04=87 ADIN06=40 ADIN24≥245 ADIN00=75	x		97 <u>+</u> 1
		ADIN04-96 ADIN06-40 ADIN24<235 ADIN00-75	х		100 <u>+</u> 1
		ADIN04=98 ADIN06=40 ADIN24>245 ADIN00=75	х		100±1
		ADIN04-110 ABIN88-93	х		138+1

Table 3.2.2.3.2 (Sheet 1 of 2)

TEST P.	ARAMETER	1		TEST PARAM	ETER
NAME	TEST PANEL INPUT	INITIAL CONDITONS	INCREASE	DECREASE	FAULT THRESHOLD
Engine 2 Torque	ADINO5	(Same tests as above, substituting ADINO1	,05+07 for	ADIN00,04-	+06 respectively
				x	232 <u>+</u> .5
ADIN24=245		ADIN24=245	Х		261 <u>+</u> .5
ADIN24=263 ADIN24 ADIN04=50 (ENG1 Torque)		ADIN24=263	х		265 <u>+</u> .5
		ADINO4=50 (ENG1 Torque)		х	232±.5
Rotor RPM	ADIN24	ADIN24=240	х		250±.5
		ADIN04=50 ADIN24=252	Х		255 <u>+</u> .5
		ADINO4=50 ADIN24=260	х		262.5 <u>+</u> .5
		ADINO0=67 DIN25,34=1,1(ENG1 Cond.Lever-FLIGHT & ADIN 04=12 Ground Contact switch ON) ADIN24=240		Х	214 <u>+</u> .5

Table 3.2.2.3.2 (Sheet 2 of 2)

<u>TITLE</u>: Fault Detection: Display Functions-Multiple Parameter Faults (Engine 1&2 Failure)

PURPOSE: Verify proper display function when multiple parameter faults are input to the system.

<u>HARDWARE CONFIGURATION</u>: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Test panel analog and discrete inputs. (See Fig. 3.1.3) INSTRUMENTATION: None.

- Power up system in normal configuration and compare the display condition with Figure 3.2.2.3.1 (a).
- 2. Verification of engine failure detection algorithms proceeds as follows, using Table 3.2.2.3.3:
 - a. Prior to each test, display the engine subsystem using the "ENG DATA" display panel switch and set the analog initial conditions.
 - b. Use the "STAT ONLY" display panel switch to remove the engine subsystem from the display.
 - c. Set the remaining Initial Conditions and enable the faults by placing the discrete input (DIN) 33 in the ON position. Acknowledge any faults.
 - d. Slowly decrease the Control Parameter until the fault condition is displayed. Compare the threshold value to that in the table.
 - e. At the completion of each test, take the action indicated by the table. For tests 9,18 & 19 cycle the acknowledge switch twice in order to first display then clear the appropriate checklist. After so doing, disable all faults as with the other tests.

TEST NUMBER: 3.2.2.3.3 (con't.)

ACCEPTANCE CRITERIA: The Engine 1 and Engine 2 Failure faults shall be activated at the appropriate threshold indicated by Table 3.2.2.3.3 for each test. In addition the following display characteristics shall be observed:

1. For tests 1--8 the boxed in warning message

ENG 1 OUT

shall be positioned in the top left corner of the Warning Message Area described by Figure 3.2.2.3.3(a). Also, the ENG 1 Torque, N1, and TGT scale indicators shall be flashing and the latter two will also be oversized and filled in.

2. For tests 10-17 observe a boxed in warning message ENG 2 OUT

positioned in the upper right corner of the Warning Message Area. The ENG 2 Torque, N1 and TGT scale indicators shall behave as described above for tests 1-8.

- 3. For tests 9 & 18 observe that after enabling the faults, oil pressure and temperature, N1, TGT & CTL faults are displayed for the appropriate engine. When the Engine Out fault is detected however, the oil pressure and temperature scale indicators shall return to normal size, not flashing or filled in.
- 4. For test 9, acknowledging the fault shall remove the warning message, stop all symbols except CTL from flashing and replace the oil pressure and temperature scales with the checklist in Figure 3.2.2.3.3(b). The same is true for test 18 except the checklist is that of Figure 3.2.2.3.3(c).
- 5. For test 19, the symbology related to both engine failures shall appear simultaneously when the fault enable operation

TEST NUMBER: 3.2.2.3.3 (con't.)

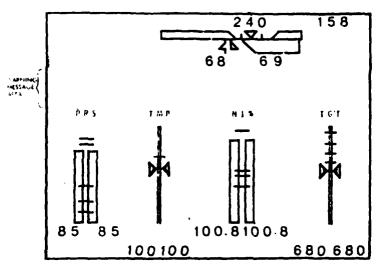
is completed. The response to acknowledgement shall be as described in 4. above except the checklist is that shown in Fig. 3.2.2.3.3(d).

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At Fault Completion				DIN 33=0	Disable)				DIN 32=1→0 (Acknowledge)	espectively)	4-6 respectively)	espectively)	DIN 32=1→0
Fault Threshold	<70+1	<400 + 1	<10+1	<65±1	<350±1	<1	<60+1	<399+1	<1 <1	s for Tests 1-3 respectively)	for Tests	s for Tests 7-9 respectively)	N/A
Control Parameter	N	TGT	Torque	N1	TGT	Torque	N1	TGT	Torque	(Same as	(Ѕате аѕ	(Same as	N/A
tions Other	; ; p == 0/ (-30 MIG	Lever in FLIGHT)	ADIN24=245 (KOTOT KPM) ADIN06= 40 (Oil Press)	0/1/0 014	Lever in GROUND)	ADIN06= 40(0il Press)	DIN 24,25=1,1(Cond.	ADIN24=245 (Rotor RPM)	ADINO6=130 (Oil Press) ADINO8=145 (Oil Temp) ADIN24=245 (Rotor RPM) DIN 24,25=1,1	(Same as above substituting ADIN 01,03,05 & 07 for ADIN00,02,04&06 & DIN 26&27 for DIN24&25 respectively	substitutions listed above)	substitutions listed above, ead of ADINO6&08 in Test18)	ADINO0=65 ADINO2=300 ADINO4= 5 DIN 25,27=1,1
ial Conditions Torque	ADIN04= 5	ADINO4= 5	ADIN04=15	ADIN04=0	ADIN04=0	ADINO4= 5	ADINO4=0	ADINO4=0	5	tuting AD 26&27 for	ing substi	ing substi instead of	ADINO4= 5
Initial TGT To	ADIN02=350	ADIN02='50	ADIN02=350	ADINO0=70 ADIN02=300	ADIN02=370	ADIN02=300	ADIN02=350	ADIN02=450	ADINO0=55 ADINO2~350 ADINO4=	(Same as above substitut:	as Tests 4-6 using	(Same as Tests 7-9 using plus using ADINO7609 inst	ADIN02=300
NI	ADIN00-75	ADIN00-65	ADIN00=65	ADIN00=70	ADIN00=60	ADIN00=60	ADIN00=65	ADIN00=55	AD IN00-55	(Same as a ADINOO 02,	(Same as T	(Same as T plus using	ADIN00=65
Test No.	1	2	3	7	5	9	7	8	6	10-12	13-15	16-18	19
Eng No.					-				43		7		16.2

Table 3.2.2.3.3



(a) General Display showing Warning Message Area

COLL-ADJ NR ENG2 BP-INC EXT LD-REL LNDG-BEGIN CND LV1-STP FIRE HNDL-1 XFEED-OPEN

(b) Engine 1 Out
Emergency
Checklist

C O L L - A D J N R E N G 1 B P - I N C E X T L D - R E L L N D G - B E G I N C N D L V 2 - S T P F I R E H N D L - 2 X F E E D - O P E N

(c) Engine 2 Out
Emergency
Checklist

COLL-ADJ NR EXT LD-REL LNDG-BEGIN CND LV1-STP CND LV2-STP FIRE HNDL-1 FIRE HNDL-2

(d) Dual Engine Out
Emergency
Checklist

Figure 3.2.2.3.3

TITLE: Fault Detection: Display Functions-Fault Priority Handling

PURPOSE: Verify proper handling by EMMADS of various priority faults.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Various test panel inputs. (See Fig. 3.1.3)

INSTRUMENTATION: None.

- 1. Power up system in normal configuration and compare display condition with Figure 3.2.2.3.1(a).
- 2. Select the engine subsystem for display using the "ENG DATA" display panel switch.
- 3. Set the following initial conditions:
 - a. ADIN 00=98 (Eng 1 N1)
 - b. ADIN 02=680 (Eng 1 TGT)
 - c. ADIN 04=75 (Eng 1 Torque)
 - d. ADIN 06=60 (Eng 1 Oil Pressure)
 - e. ADIN 08=100 (Eng 1 0il Temperature)
 - f. ADIN 24=245 (Rotor RPM)
 - g. DIN 25,27=1,1 (Eng 1 & 2 Condition Levers in FLIGHT)
- 4. Clear the engine subsystem by depressing the "STAT ONLY" display panel switch. Enable all faults by placing DIN 33 ON and compare the display with Figure 3.2.2.3.4(a). Observe the Eng2 Torque, Nl and TGT scale indicators and the word "ENG" in the upper left display corner all flash.
- 5. Decrease rotor rpm (ADIN 24) to 230 and compare the display with Figure 3.2.2.3.4(b). Observe rotor rpm pointer now is also flashing.
- 6. Acknowledge the rotor rpm fault (DIN 32 ON then OFF) and

TEST NUMBER: 3.2.2.3.4 (con't.)

- compare display with Figure 3.2.2.3.4(c). Observe rotor rpm pointer not flashing.
- 7. Acknowledge the rotor rpm checklist (DIN 32 ON then OFF) and compare display with Figure 3.2.2.3.4(d).
- 8. Acknowledge the ENG 2 Failure fault (DIN 32 ON then OFF) and compare display with Figure 3.2.2.3.4(e). Note all flashing ceases.
- 9. Set DIN 20 and DIN 21 to ON (ENG 1 & 2 Oil Quantity
 Low) and compare display to Fig. 3.2.2.3.4(e). Note that
 the only change is that the word "ENG" again begins to
 flash.
- 10. Acknowledge the engine failure checklist (DIN 32 ON then OFF) and compare display with Figure 3.2.2.3.4(f).
 Note that the words "ENG" and QTY" flash.
- 11. Acknowledge the "QTY" faults and compare display with Figure 3.2.2.3.4(g).

ACCEPTANCE CRITERIA: The display symbology shall correspond to the figures and other criteria cited in each Test Procedure step.

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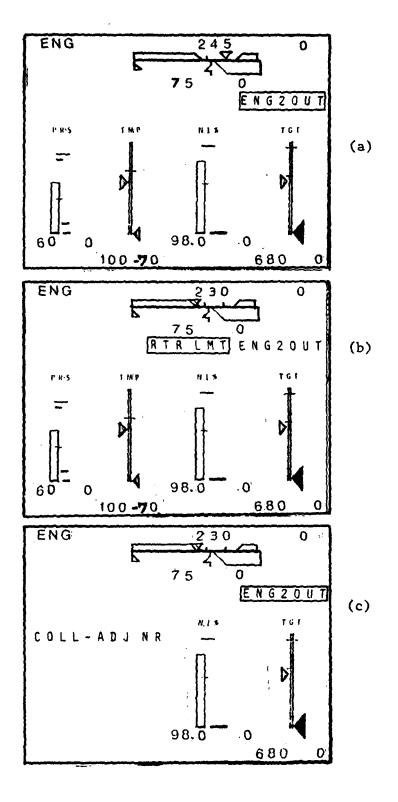


Figure 3.2.2.3.4

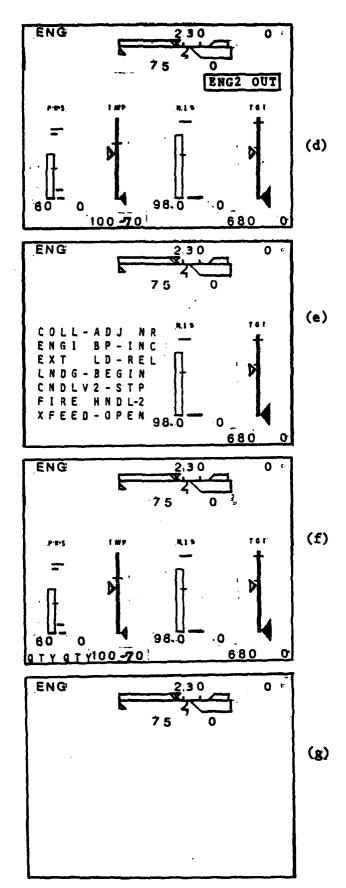


Figure 3.2.2.3.4 (con't)

TITLE: Fault Detection: Display Functions-Timed Parameter Limit Clock.

PURPOSE: Verify proper operation of the Timed Parameter Limit Clock.

HARDWARE CONFIGURATION: All units with standard interconnections.

SOFTWARE REQUIRED: Operational programs for EMMADS and Emulator.

STIMULI: Various test panel inputs. (See Fig. 3.1.3)

INSTRUMENTATION: None.

- 1. Power up system in normal configuration and compare with Figure 3.2.2.3.1(a).
- 2. Select the engine subsystem for display using the "ENG DATA" display panel switch.
- 3. Set the following initial conditions:
 - a. ADIN 02 & 03 = 700 (ENG 1 & 2 TGT)
 - b. ADIN 04 & 05 = 30 (ENG 1 & 2 Torque)
 - c. ADIN 24 = 245 (Rotor RPM)
 - d. DIN 24.25.26 & 27 = 1 (Both cond. levers out of detent)
- 4. Verification of Timed Parameter Limit Clock proceeds as follows, using Table 3.2.2.3.5:
 - a. With faults enabled (DIN 33=1) and acknowledged, begin each test by changing the test parameter(s) to the indicated value(s).
 - b. At the indicated parameter thresholds, note the initial clock value and label (which occupy the display positions shown in Fig. 3.2.2.3.5) and compare with those indicated in the table.
 - c. When the clock value reaches that shown in the table for test termination, take the indicated action to terminate the test. Unless otherwise specified in the table, if the test concludes with DIN 33=0 (Faults Disabled) reset the test parameter(s) to the initial values in step 3 above before turning DIN 33 back ON for the next test.

TEST NUMBER: 3.2.2.3.5 (con't.)

ACCEPTANCE CRITERIA: The initial clock values and the clock labels shall be as indicated in Table 3.2.2.3.5. In cases where a test is terminated by allowing the countdown clock to time out, the label and scale indicator(s) of the test parameter shall flash until the fault is acknowledged. In certain cases where the word "(nominal)" proceeds the initial clock value for a given test, this indicates that the clock value depends in part on how quickly the test operator can readjust the test parameter value from the previous test. The clock time is therefore approximate to within one minute.

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COUNTDOWN CLOCK

TEST	TEST PARAMETER	INCREASE/ DECREASE TO	PARAMETER THRESHOLD	INITIAL CLOCK VALUE	LABEL	CLOCK VÁLUE	ACTION
-	Eng 1&2 Torque	80 (Seperate)	78+1	00:10	TRQ 1. TRQ 2	00:00	Set ADINO0 = 75% then Acknowledge (DIN 32=1-00) and set ADINO4=80
~		06	85±1	30:00		28:30	Begin Test 3 by increasing torque to given value
٦		86	1+16	10:00		8:30	Begin Test 4 by increasing torque to given value
4	Eng 1 Torque	110	1001	00:10	TR () 1	:05	Begin Test 5 by decreasing torque to given value
5		86	1 + 001	(nominal)		8:00	Begin Test 6 by decreasing torque to given value
٥		06	1-16	26:30		26:00	DIN 33-0, ADINO4 & 05 - 30, ADINO1 - 75%
7-11	Eng 2 Torque	(Same as for T	ests 2-6 re	Tests 2-6 respectively)	TRQ 2	(Same as for Tests 2-6)	(Same as for Tests 2-5 respectively. For Test 11 do not disable faults with DIN33 but begin Test 12)
22	Rotor RPM	253	251±.5	02:00	RTR	04:30	Begin Test 13 by increasing rotor to given value
23	Rotor RPM	260	256±.5	00:02	RTR	00:00	Acknowledge (DIN 32-1-00) then go to Test 14
14	Eng 2 Torque	N/A	N/A	25:25 (nominal)	TRQ 2	24:00	DIN 33=0
15		800	77041	30:00		28:30	Begin Test 16 by increasing TGT to given value
16		840	810-1	10:00		8:30	Begin Test 17 by increasing TGT to given value
17	Eng 1 TGT	880	1 - 098	00:02	rcr 1	00:00	Begin Test 18 by decreasing TGT to given value
18		800	810-1	27:00 (nominal)		26:00	DIN 33-0, DIN 14,28 & 30 = 1
22		НАХ	788-1	00:04		00:00	DIN 33=0
21- 26	ENG 2 TCT	(Same as Tes	ts 15-22 re	ts 15-22 respectively)	TGT 2	(Same as f	(Same as for Tests 15-22 respectively)

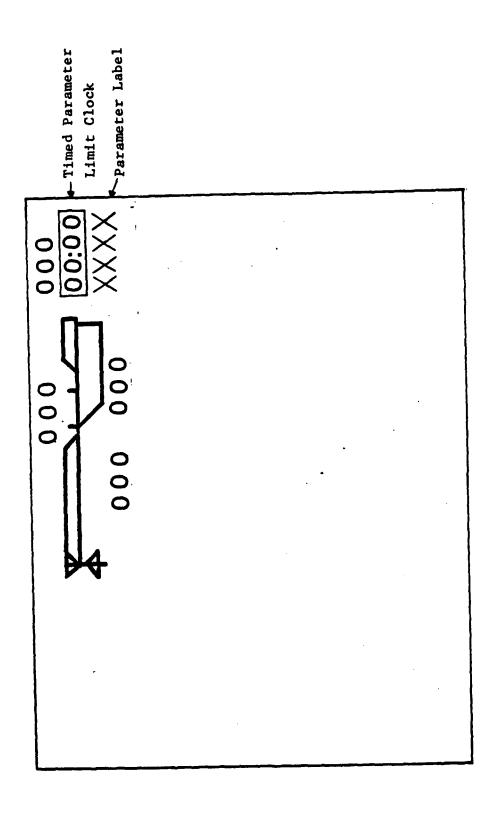


Figure 3.2.2.3.5

